



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

**Masataka HOSHINO**

Group Art Unit: 2823

Serial No.: **09/982,963**

Examiner: **COLEMAN, William D.**

Filed: **October 22, 2001**

P.T.O. Confirmation No.: **8557**

For: **THREE DIMENSIONAL SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING A PIERCING ELECTRODE**

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: October 24, 2003

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated July 11, 2003 for one month from October 11, 2003 to November 11, 2003.

Attached please find a check in the amount of \$110.00 to cover the cost of the extension for a large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 50-2866.

Respectfully submitted,

WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP

Sadao Kinashi  
Attorney for Applicant  
Reg. No. 48,075

10/28/2003 AWOHDAF1 00000058 09982963

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SK/fs

Atty. Docket No. **011354**  
1250 Connecticut Ave. N.W.  
Suite 700  
Washington, D.C. 20036  
(202) 822-1100

**38834**

PATENT TRADEMARK OFFICE

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